

APPL NUM 10092875	FILING DATE 03/05/2002	CLASS 257.478	SUBCLASS 396	GAU 2841	2929	EXAMINER K. L. G.
<b>**APPLICANTS:</b> Morgan Paul; Flynn Patrick; Fucsko Janos; + 689						
<b>**CONTINUING DATA VERIFIED:</b> LAK none 6/10/03   LAK none 11/20/03						
<b>**FOREIGN APPLICATIONS VERIFIED:</b> LAK none 6/10/03   LAK none 11/20/03						
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO		
Foreign priority claimed <input type="checkbox"/>		35 USC 119 conditions met <input type="checkbox"/>		Verified and Acknowledged Examiners' initials <i>W. L.</i>		
TITLE : Etching processes for integrated circuit manufacturing including methods of forming capacitors <small>U.S. DEPT. OF COM. / PAT. &amp; TM. / PTO-435 (Rev. 12-94)</small>						

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